

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L2	961	(257/510 or 257/513).ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/04 19:06
L5	1181	(257/647 or 257/659).ccls. not L4 not L2	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/04 19:07
L4	516	(257/395-397).ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/04 19:07
L7	11163	semiconductor and (trench\$4 or recess\$4 or groove or STI) and ((liner or layer) same ((nitride or SiN) with (cvd or (vapor adj deposit\$))))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/04 19:34
L6	12140	semiconductor and (trench\$4 or recess\$4 or groove or STI or locos or fox) and ((liner or layer) same ((nitride or SiN) with (cvd or (vapor adj deposit\$))))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/04 19:34
L9	44	semiconductor and (((trench\$4 or recess\$4 or groove or STI) with ((liner) near7 (top or above or over))) same ((nitride or SiN) with (cvd or (vapor adj deposit\$))))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/04 19:38
L8	638	semiconductor and (((trench\$4 or recess\$4 or groove or STI) with ((liner or layer) near7 (top or above or over))) same ((nitride or SiN) with (cvd or (vapor adj deposit\$))))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/04 19:43
L10	594	semiconductor and (((trench\$4 or recess\$4 or groove or STI) with ((liner or layer) near7 (top or above or over))) same ((nitride or SiN) with (cvd or (vapor adj deposit\$)))) not L9	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/04 19:44